

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L8	256	rapid ADJ (io I?O)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/27 12:46
L9	10	L8 SAME FPGA	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/27 12:46
L10	4	(recieve download) NEAR2 (second new) NEAR2 (firmware configuration) NEAR2 (default original first)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/27 13:35
L11	95	(only) NEAR2 (second new) NEAR2 (firmware configuration) NEAR2 (default original first)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/27 13:35
L12	3	11 AND FPGA	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/27 13:35
L13	60	(hardwire\$2 NEAR4 pipeline)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/27 13:46
L14	44	((JOHN) near2 (RAPP)).INV.	US-PGPUB; USPAT	OR	ON	2007/07/27 14:02
L15	46	((LARRY) near2 (JACKSON)).INV.	US-PGPUB; USPAT	OR	ON	2007/07/27 14:02
L16	300	((MARK) near2 (JONES)).INV.	US-PGPUB; USPAT	OR	ON	2007/07/27 14:02
L17	5	((TROY) near2 (CHERASARO)).INV.	US-PGPUB; USPAT	OR	ON	2007/07/27 14:02
L18	909098	(firmware driver (operating near3 (instruction program software code)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/27 14:09
L19	646561	(\$1PGA (programmable adj gate adj array)) (PLU (programmable adj logic adj unit)) (programmable near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/27 14:09
L20	5163854	(memory stor\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/27 14:09

## EAST Search History

L21	10813216	(multipl\$5 two three four many plural\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/27 14:09
L22	30	((L21 NEAR2 L18) WITH L20) SAME L19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/27 14:09
L23	347	(hardware WITH pipeline) AND FPGA	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/27 14:09
L25	2	("6704816" "20040181621").pn.	US-PGPUB; USPAT	OR	ON	2007/07/27 14:15